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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/684,782	10/10/2000	John W. McCorkle	197024US8	1981

7590

12/24/2003

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4th Floor  
1755 Jefferson Davis Highway  
Arlington, VA 22202

EXAMINER
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CORRIELUS, JEAN B

ART UNIT	PAPER NUMBER
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2631

DATE MAILED: 12/24/2003

8

Please find below and/or attached an Office communication concerning this application or proceeding.

## Office Action Summary

Application No.

09/684,782

Applicant(s)

MCCORKLE, JOHN W.

Examiner

Jean B Corrielus

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

### Status

- 1) ☒ Responsive to communication(s) filed on 10 October 2000.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

### Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-3 and 7-11 is/are allowed.
- 6) ☒ Claim(s) 4-6 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

### Priority under 35 U.S.C. §§ 119 and 120

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a) ☐ All b) ☐ Some \* c) ☐ None of:  
1. ☐ Certified copies of the priority documents have been received.  
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
\* See the attached detailed Office action for a list of the certified copies not received.
- 13) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application) since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.  
a) ☐ The translation of the foreign language provisional application has been received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121 since a specific reference was included in the first sentence of the specification or in an Application Data Sheet. 37 CFR 1.78.

### Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 4 and 6. 6) ☐ Other: \_\_\_\_\_

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## **DETAILED ACTION**

### ***Specification***

1. Please update the status of the related applications, mentioned in the specification.

### ***Claim Objections***

2. Claim 4, which element in the drawing corresponds to the claimed “self-noise suppression means” and “detection waveform generation means”. Claim 4, line 2, “receiver” is mistyped as “reciever”. Appropriate correction is required.

### ***Claim Rejections - 35 USC § 102***

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35

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U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

4. Claim 4 is rejected under 35 U.S.C. 102(e) as being anticipated by Dress, Jr. et al. US Patent No. 6,603,818.

Dress, Jr. discloses fig. 14 means 1420 functionally equivalent to the claimed self noise suppression means for suppressing noise generated by the receiver; means 1460 considered as the claimed detection waveform generating means coupled to the means 1420 (self noise suppression means) for receiving ultra wideband wavelets via an antenna and generating a detection waveform having encoded therein transmitted data source (see fig. 13).

***Claim Rejections - 35 USC § 103***

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Dress, Jr. et al in view of McEwan US patent No. 5,523,760.

As applied to claim 1 above, Dress et al discloses every feature of the claimed invention but does not explicitly teach an integrator having a signal input coupled to means 1460 (detection waveform ) and a signal output used to decode the detection waveform. In the same field of

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endeavor McEwan discloses fig. 2A an integrator having a signal input coupled to detection waveform 12 and a signal output 16 use to decode the detection waveform. Given that fact, it would have been obvious to one skill in the art at the time of the invention to incorporate such a teaching in Dress, Jr. et al in order to provide high sensitivity as taught by McEwan see col. 4, lines 45-48.

7. Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Dress, Jr. et al in view of Na US Patent No. 6,112,069.

As applied to claim 1 above, Dress et al discloses every feature of the claimed invention but does not explicitly teach a DC bias blocking means for blocking DC bias component of signals output by the self noise suppression means. In the same field of endeavor Na discloses fig. 4 DC bias blocking means (115 and 123) for blocking DC bias component of signals output by the self noise suppression means (113). Given that fact, it would have been obvious to one skill in the art at the time of the invention to incorporate such a teaching in Dress, Jr. et al in order to block DC voltage offset due undesired voltage or interference and DC offset voltage due to bias within the circuit as taught by Na see col. 4, lines 6-8.

***Allowable Subject Matter***

8. Claims 1-3 and 7-11 are allowed.

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9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean B. Corrielus whose telephone number is (703) 305-4023.

The examiner can normally be reached on Monday-Thursday from 7:00 A.M. to 5:30 P.M.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3800.

  
Jean B. Corrielus

Primary Examiner

TC-2600 12-16-03